



501.3/2049R00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yujiro KAJIHARA *et al.*

Serial No.: 09/328,910

Filed: 9 June 1999

For: LEAD FRAME SEMICONDUCTOR, INTEGRATED
CIRCUIT DEVICE USING THE SAME, AND METHOD
OF AND PROCESS FOR FABRICATING THE TWO

Group: 2815

Examiner: Jasmine Jhihan B. CLARK

Conf. No.: 7455

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AMENDMENT GROUP 3600

TECHNOLOGY CENTER 2800

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Mail Stop Reissue

Commissioner for Patents

POB 1450

Alexandria, Virginia 22313-1450

2 September 2003

Sir:

In response to the Office Action having a USPTO mailing date of 31 March 2003, the following is respectfully submitted in connection with the above-identified application. In accordance with the revised format of the manner of making amendments under 37 CFR §1.121 as set forth in the Final Rule effective 30 July 2003, each section of amendment herein begins on a new page, with changes in the original patent text shown by strike-through (or double brackets where appropriate) and underlining to indicate deletions and additions (including text not in the original patent), respectively.